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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/552,261

10/05/2005

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6192.0643.US

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ART UNIT

PAPER NUMBER

2629

MAIL DATE

DELIVERY MODE

07/24/2008

PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b> 10/552,261	<b>Applicant(s)</b> SHIN ET AL.	
	<b>Examiner</b> MY-CHAU T. TRAN	<b>Art Unit</b> 2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 03 June 2008.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-41 is/are pending in the application.
- 4a) Of the above claim(s) 17-41 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,2 and 8-16 is/are rejected.
- 7) ☒ Claim(s) 3-7 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 05 October 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All    b) ☐ Some \*    c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)            | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | Paper No(s)/Mail Date. _____                                      |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date <u>10/05/2005</u> .  | 6) <input type="checkbox"/> Other: _____                          |

## **DETAILED ACTION**

### ***Application and Claims Status***

1. Applicant's response filed 06/03/2008 are acknowledged and entered.
2. Claims 1-41 were pending. No claims were amended, added, and/or cancelled.

Therefore, claims 1-41 are currently pending.

### ***Election/Restrictions***

3. Applicant's election without traverse of Group I (Claims 1-16) in the reply filed on 06/03/2008 is acknowledged.
4. Claims 17-41 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to ***nonelected inventions***, there being no allowable generic or linking claim. Election was made **without** traverse in the reply filed on 06/03/2008. Accordingly, claims 1-16 are under consideration in this Office Action.

### ***Priority***

5. Receipt is acknowledged of papers, i.e. Korean Application No. 10-2003-0021640 filed 04/07/2003, and Korean Application No. 10-2004-0022553 filed 04/01/2004, submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

### ***Information Disclosure Statement***

6. The information disclosure statement (IDS) filed on 10/05/2005 has been reviewed, and the references that have been considered are initialed as recorded in PTO-1449 form.

### ***Drawings***

7. Figures 1 and 2 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. Here, the instant specification discloses that figures 1 and 2 are illustrations of conventional (known) organic electroluminescent panel (see specification pgs. 1, line 24 thru pg. 2, line 24; pg. 5, lines 10-13). See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

### ***Claim Objections***

8. Claim 5 is objected to under 37 CFR 1.75 as being a substantial duplicate of claim 3. When two claims in an application are duplicates or else are so close in content that they both cover the same thing, despite a slight difference in wording, it is proper after allowing one claim to object to the other as being a substantial duplicate of the allowed claim. See MPEP § 706.03(k). Here, claim 5 recites the limitation of '*The display panel of claim 2, further comprising a pixel electrode electrically connected to the driving part, and wherein the first current supply line and the data line are formed from a first layer, and the second current supply line and the scan line are formed from a second layer*'. Claim 3 recites the limitation of '*The display panel of claim 2, further comprising a pixel electrode electrically connected to the driving part, and wherein the first current supply line and the data line are formed from a first*

*layer and the second current supply line and the pixel electrode are formed from a second layer’.*

Consequently, claim 5 is exactly the same as claim 3.

### ***Claim Rejections - 35 USC § 102***

9. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

10. Claims 1, 2, and 9-16 are rejected under 35 U.S.C. 102(b) as being anticipated by Inukai (US Patent Application Publication US 2001/0038367 A1).

For ***claims 1, 2, and 9-16***, Inukai disclose an active matrix EL (electroluminescence) display device (see e.g. Abstract; sections: [0002], [0036], [0069] thru [0076]; figs. 1-3). As illustrated by figure 1, the display device comprises a pixel portion (ref. #101), a gate signal line driver circuit (ref. #103), a source signal line driver circuit (ref. #102), and an opposing power source line driver circuit (ref. #104) (see e.g. sections: [0069]-[0070]). The pixel portion (ref. #101) (refers to instant claimed display panel) comprises a plurality of source signal line (ref. #S1 to Sx) (refers to instant claimed data line) that provide the data signal, a plurality of gate signal line (ref. #G1 to Gy) (refers to instant claimed scan signal line) that provide the scan signal, a plurality of power source line (ref. #V1 to Vx) (refers to instant claimed current supply line/first current supply line and instant claims 9-12), a plurality of opposing power source line (ref. #E1 to Ey) (refers to instant claimed current supply line/second current supply line and instant claims 9-12), and a plurality of pixel (ref. #105) that is arranged in a matrix region

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wherein each region having one each of a source signal line, gate signal line, power source line, and opposing power source line (refers to instant claim 14) (see e.g. section: [0071]-[0086]; fig. 3). Each pixel comprises a switching TFT (ref. #107) (refers to instant claimed switching part) that is connected to the source signal line and the gate signal line; an EL driver TFT (ref. #108) (refers to instant claimed driving part) that is connected to the power source line, the switching TFT, and the EL element; a capacitor (ref. #112); and an EL element, which includes an organic compound sandwiched between a pair of electrodes (anode and cathode) (refers to instant claimed organic electro luminescent part) wherein the anode is connected to the EL driver TFT and the cathode is connected to the opposing power source line (see e.g. section: [0006]-[0007] and [0071]-[0086]; figs. 2 and 3). The type of TFT for both the switching TFT and the EL driver TFT include either the n-channel TFT or the p-channel TFT (see e.g. section: [0081]) (refers to instant claims 15 and 16).

Therefore, the device of Inukai does anticipate the instant claimed invention.

### ***Claim Rejections - 35 USC § 103***

11. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

12. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any

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evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

13. Claims 1, 2, and 8-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Inukai (US Patent Application Publication US 2001/0038367 A1).

For *claims 1, 2, and 9-16*, Inukai disclose an active matrix EL (electroluminescence) display device (see e.g. Abstract; sections: [0002], [0036], [0069] thru [0076]; figs. 1-3). As illustrated by figure 1, the display device comprises a pixel portion (ref. #101), a gate signal line driver circuit (ref. #103), a source signal line driver circuit (ref. #102), and an opposing power source line driver circuit (ref. #104) (see e.g. sections: [0069]-[0070]). The pixel portion (ref. #101) (refers to instant claimed display panel) comprises a plurality of source signal line (ref. #S1 to Sx) (refers to instant claimed data line) that provide the data signal, a plurality of gate signal line (ref. #G1 to Gy) (refers to instant claimed scan signal line) that provide the scan signal, a plurality of power source line (ref. #V1 to Vx) (refers to instant claimed current supply line/first current supply line and instant claims 9-12), a plurality of opposing power source line (ref. #E1 to Ey) (refers to instant claimed current supply line/second current supply line and instant claims 9-12), and a plurality of pixel (ref. #105) that is arranged in a matrix region wherein each region having one each of a source signal line, gate signal line, power source line, and opposing power source line (refers to instant claim 14) (see e.g. section: [0071]-[0086]; fig. 3). Each pixel comprises a switching TFT (ref. #107) (refers to instant claimed switching part) that is connected to the source signal line and the gate signal line; an EL driver TFT (ref. #108)

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(refers to instant claimed driving part) that is connected to the power source line, the switching TFT, and the EL element; a capacitor (ref. #112); and an EL element, which includes an organic compound sandwiched between a pair of electrodes (anode and cathode) (refers to instant claimed organic electro luminescent part) wherein the anode is connected to the EL driver TFT and the cathode is connected to the opposing power source line (see e.g. section: [0006]-[0007] and [0071]-[0086]; figs. 2 and 3). The type of TFT for both the switching TFT and the EL driver TFT include either the n-channel TFT or the p-channel TFT (see e.g. section: [0081]) (refers to instant claims 15 and 16).

The teachings of Inukai differ from the presently claimed invention as follows:

For **claim 8**, Inukai fail to disclose that the ‘*storage capacitor disposed between the first current supply line and the driving part*’. However, it would have been obvious to a person of ordinary skill in the art at the time the invention was made to disclose that the ‘*storage capacitor disposed between the first current supply line and the driving part*’, since Inukai disclose that it is art recognized wherein the capacitor can be placed between the current supply line and the driving TFT (see fig. 23). Moreover, in both the prior art pixel structure (see fig. 23) and the pixel structure of Inukai (see fig. 3) the capacitor is connected to the current supply line (ref. #V), and as a result the placement of the capacitor, i.e. between the current supply line and the driving TFT, would be a choice of experimental design and is considered within the purview of the cited prior art.

Therefore, the teachings of Inukai do render the device of the instant claims *prima facie* obvious.



*Allowable Subject Matter*

14. Claims 3-7 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

*Conclusion*

Any inquiry concerning this communication or earlier communications from the examiner should be directed to MY-CHAU T. TRAN whose telephone number is (571)272-0810. The examiner can normally be reached on Monday: 8:00-2:30; Tuesday-Thursday: 7:30-5:00; Friday: 8:00-3:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard A. Hjerpe can be reached on 571-272-7691. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/MY-CHAU T. TRAN/  
Primary Examiner, Art Unit 2629

July 24, 2008